

FIG. 1

FIG. 2 is a block diagram of a communication system 100. The system 100 includes a transmitter 138, a receiver 150, a controller 122, a memory 124, a location detector 164, a display 128, a keypad 130, a control unit 132, an antenna 134, and a speaker 136. The transmitter 138 includes an analog-to-digital converter 140, a speech codec 144, a channel codec 146, a digital signal processor 142, and a phase modulator and RF amplifier 148. The receiver 150 includes a receiver/amplifier 152, a digital signal processor 154, a digital-to-analog converter 156, and an antenna 162. The system 100 also includes a duplexer 160 and a location detector 164. The system 100 is connected to a network 158.

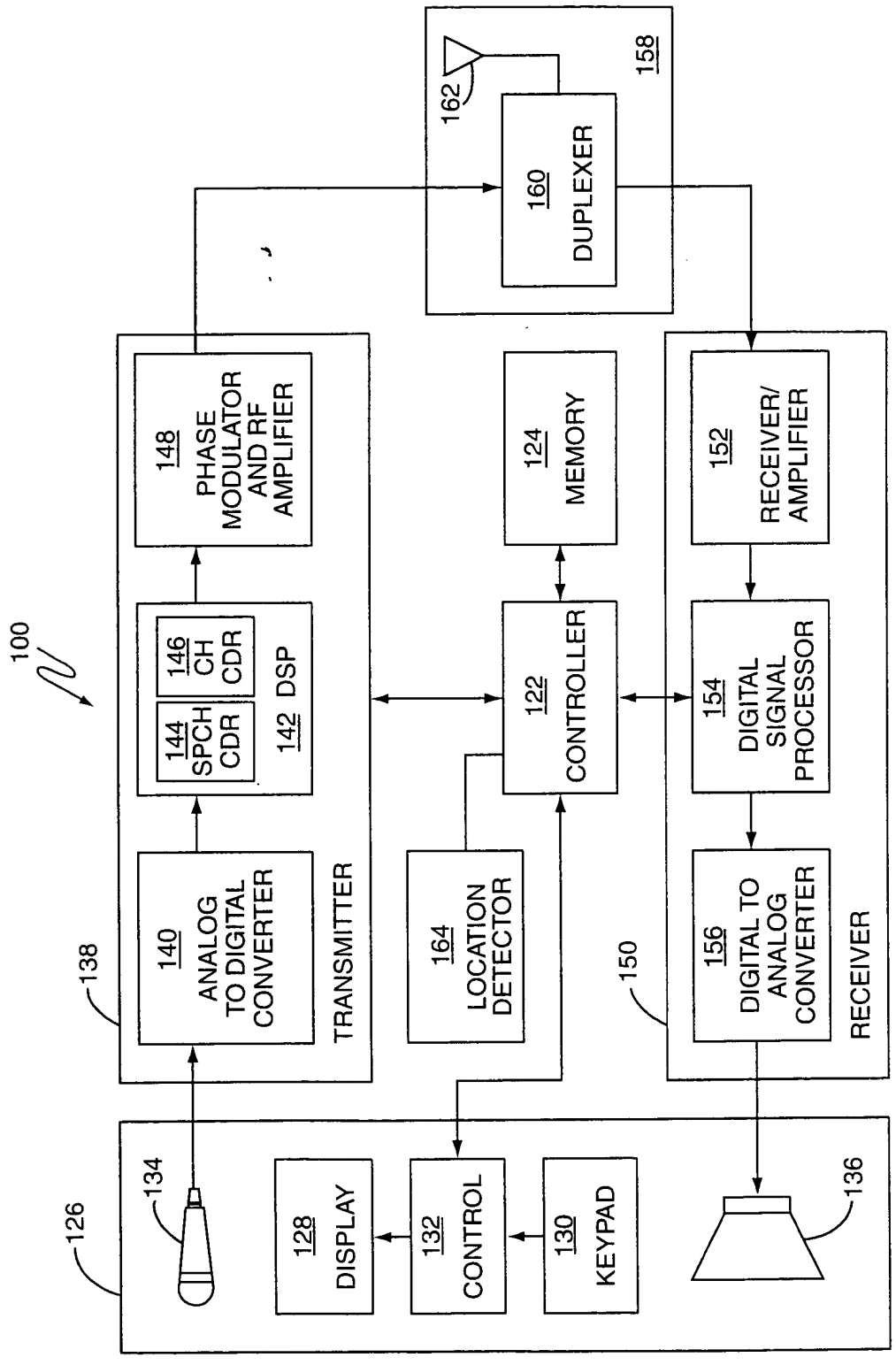


FIG. 2

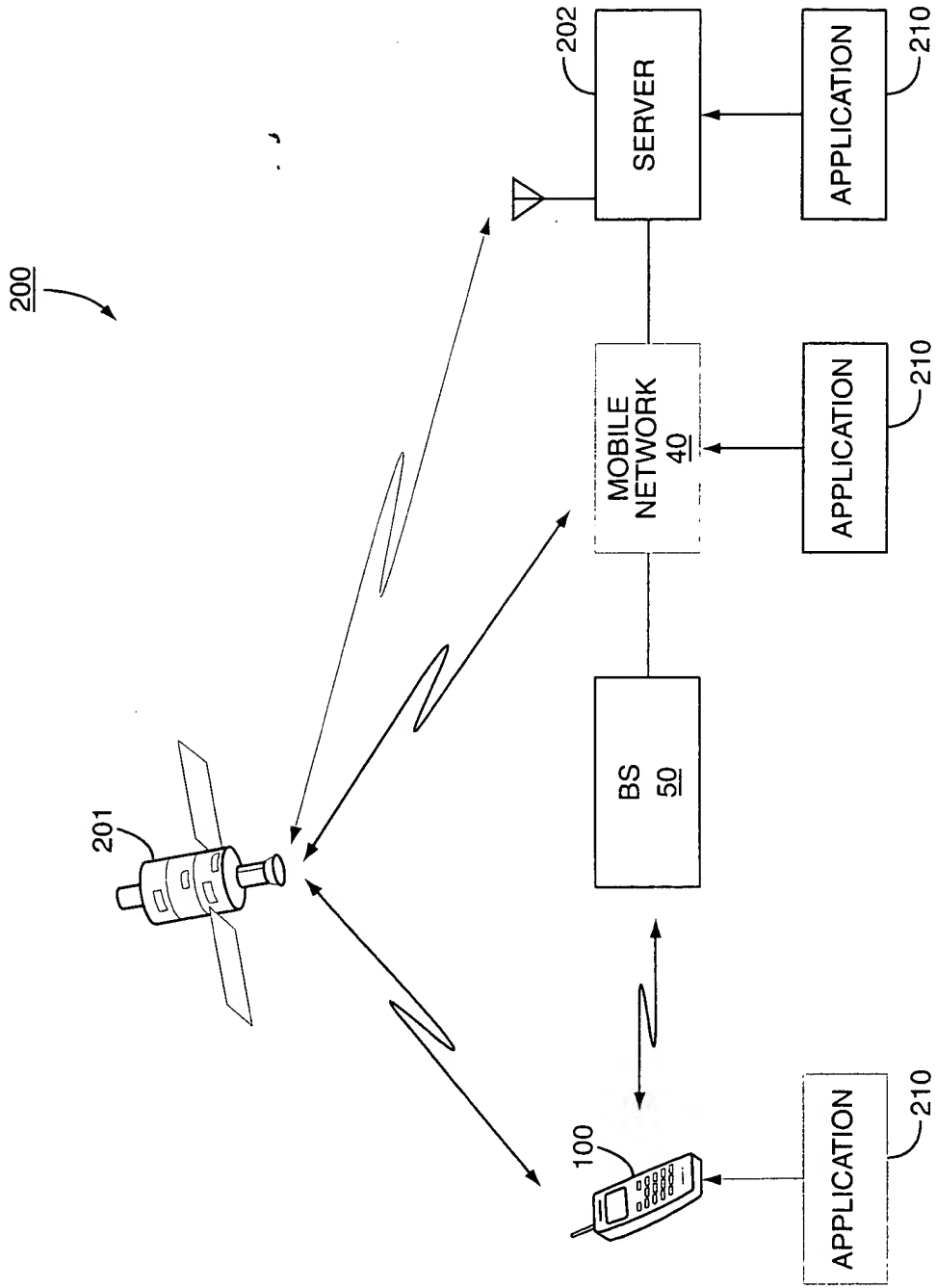
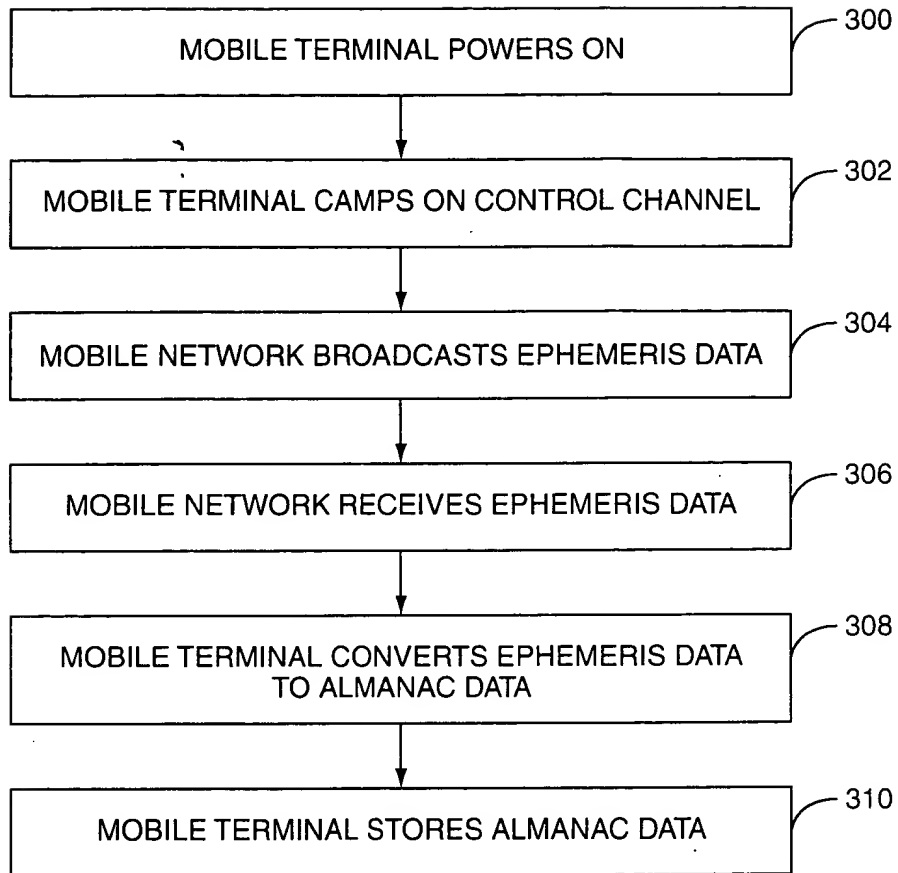
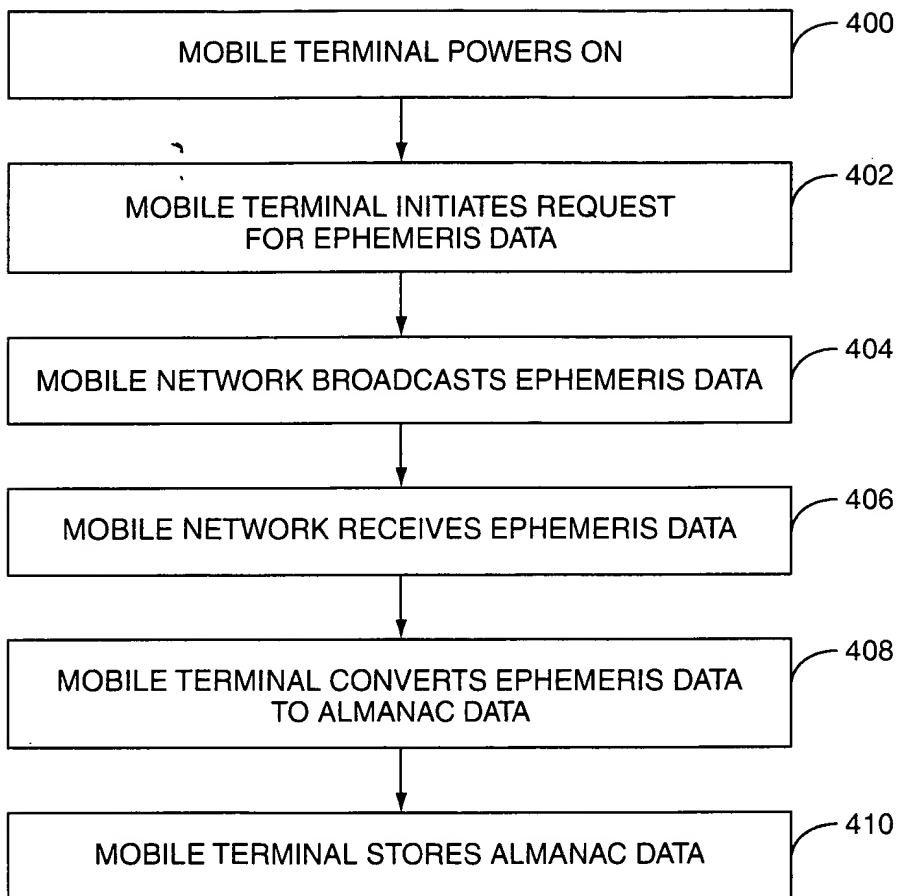


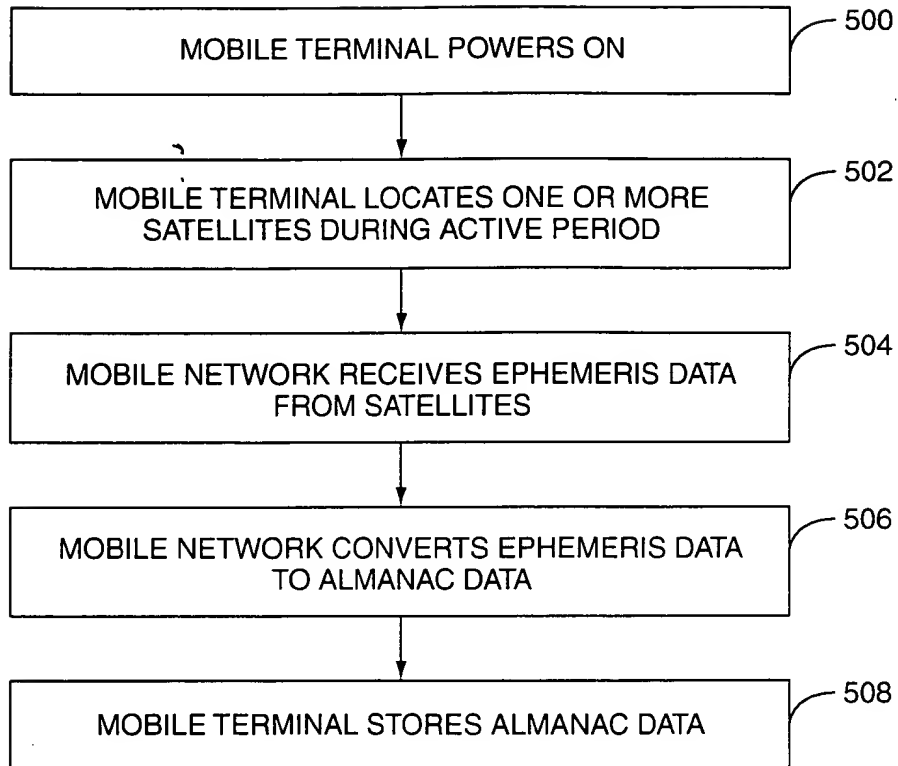
FIG. 3



**FIG. 4**



**FIG. 5**



**FIG 6**